The opinion in support of the decision being entered today is *not* binding precedent of the Board.

#### UNITED STATES PATENT AND TRADEMARK OFFICE

# BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

Ex parte MASAHIKO NAKAYAMA

Appeal 2007-3837 Application 10/631,897<sup>1</sup> Technology Center 2100

Decided: September 26, 2007

Before JAMESON LEE, SALLY C. MEDLEY, and JAMES T. MOORE, Administrative Patent Judges.

MEDLEY, Administrative Patent Judge.

#### **DECISION ON APPEAL**

- 1 A. Statement of the Case
- 2 Applicant's appeal under 35 U.S.C. § 134 from a final rejection of
- 3 claim 5. We have jurisdiction under 35 U.S.C. § 6(b).
- The prior art relied upon by the Examiner in rejecting the claims on
- 5 appeal is:

<sup>1</sup> Application for patent filed 1 August 2003. The real party in interest is NEC Corporation (Br. 2).

1 2	Christopher	US 4,731,851	Mar. 15, 1988	
3	Claim 5 stands reject	cted under 35 U.S.C. §	102(b) as being anticipated	l
4	by Christopher (Final Reje	ection 2 and Answer 3).		
5		BACKGROUND		
6	The invention is rel	ated to a circuit that adj	usts the decibel level of an	
7	input signal. The circuit i	ncludes a plurality of pa	rallel signal lines that are	
8	shifted a number of bits (I	FIG. 5 items 221-226).	A control circuit (FIG. 5,	
9	item 250), set for the desir	red decibel level, contro	ls one or more switches	
0	(FIG. 5, items 231-233).	The switches are contro	lled by the control circuit t	C
1	select among the outputs of	of the signal lines (Speci	ification 18:7-11). If an	
12	output is not needed, a de	fault "0" is generated (F	IG. 5, item 234,	
13	Specification 19:5-10). A	dder circuits (FIG. 5 23	5-237) add the outputs	
14	from the switches to get the	ne desired decibel level	output signal (FIG. 5 item	
15	143).			
16	B. Issue			
17	The issue is whether	r Applicants have show	n that the Examiner erred	ir
18	determining claim 5 to be	unpatentable over Chris	stopher.	
19	C. Findings of fac	t ("FF")		
20	The record supports	s the following findings	of fact as well as any othe	r
21	findings of fact set forth in	n this opinion by at least	a preponderance of the	
22	evidence.			
23	1. Applicant's s	sole pending claim 5 is t	he subject of this appeal.	

1	2. The Examiner finally rejected claim 5 as being anticipated by
2	Christopher.
3	3. Claim 5 is as follows:
4	5. A decibel level adjustment device for calculating an output
5	signal which is a d decibel multiple of an input signal, comprising:
6	a plurality of signal lines arranged parallel to each other for
7	producing in advance signals that are shifted a number of bits
8	necessary for operating on said input signal;
9	at least one switch means for selecting outputs of said plurality
0	of signal lines or all "0";
1	a switch control circuit means for receiving the value of said d
2	as a decibel control value and, in accordance with said decibel control
3	value, switching said switch or switches; and
4	an adder circuit means for adding together the outputs of said
5	switch or switches and output of said signal lines that does not pass by
6	way of said switch or switches.
7	<u>Christopher</u>
8	4. The Examiner found that Christopher describes in Fig. 6, a decibel
9	adjustment device including a plurality of signal lines (503, 504, 506, 508)
20	that are shifted a number of bits (Christopher, col. 9, ll. 12-14), at least one
21	switch means (510, 512, 514), a switch control circuit means (518, C0C1)
22	and an adder circuit means (516) (Final Rejection 2 and Answer 3).

1	5. Christopher describes that the weighting circuits 504, 506 and 508
2	"may be simple hard-wired bit shift arrangements." (Christopher, col. 9, ll.
3	12-14).
4	Applicant's arguments
5	6. Applicant argues that claim 5 requires that each of the plurality of
6	signal lines is individually shifted a number of bits, and that Christopher's
7	weighting circuits 504, 506, and 508 do not act on the input signal 500 but or
8	signal 503 (Br. 9).
9	7. Applicant also argues that there is no switch in Christopher with the
10	function recited in claim 5 for selecting outputs of said plurality of signal
11	lines or all "0" (Br. 10).
12	Examiner's response
13	8. The Examiner responded and further found that Christopher
14	describes that the output of the weighting circuits 504, 506 and 508, which
15	are hardwired bit shift arrangements, operate on the input signal as claimed
16	(Answer 4).
17	9. The Examiner further found that gating elements 510, 512, and 514
18	(FIG. 6) meet the switching means limitation since the outputs of the gating
19	elements provide either signals in the parallel lines (when enabled) or
20	otherwise zero (when disabled) to the adder 502 in response to control
21	signals (Answer 5).
22	D. Principles of Law

I	35 U.S.C. § 102
2	"A person shall be entitled to a patent unlessthe invention was
3	patented or described in a printed publication in this or a foreign country or
4	in public use or on sale in this country, more than one year prior to the date
5	of the application for patent in the United States" 35 U.S.C. § 102(b).
6	To anticipate a claim, a prior art reference must disclose every
7	limitation of the claimed invention, either expressly or inherently. Verdegaal
8	Bros. Inc. v. Union Oil Co., 814 F.2d 628, 631, 2 USPQ2d 1051, 1053 (Fed.
9	Cir. 1987).
0	The Board must give a claim its broadest reasonable construction. In
1	re Bigio, 381 F.3d 1320, 1324, 72 USPQ2d 1209, 1210-11 (Fed. Cir. 2004).
2	KCJ Corp. v. Kinetic Concepts, Inc., 223 F.3d 1351, 1356, 55 USPQ2d 1835
3	1839 (Fed. Cir. 2000).
4	
5	E. Analysis
6	The Examiner finally rejected claim 5 as being anticipated under 35
7	U.S.C. § 102(b) by Christopher. The Examiner made the requisite findings,
8	applying Christopher to the claim, and explained how each limitation is met
9	by Christopher.
0	Applicant argues that Christopher fails to describe "a plurality of
1	signal lines arranged parallel to each other for producing in advance signals
2	that are shifted a number of bits necessary for operating on said input signal"
3	as recited in claim 5.

Applicant has not demonstrated error in the Examiner's finding that 1 Christopher describes a plurality of signal lines arranged in parallel as seen in 2 Fig. 6, e.g., the lines going into weighting circuits 504, 506 and 508. 3 Applicant also has not demonstrated error in the Examiner's finding that 4 weighting circuits 504, 506 and 508 function to shift their respective inputs a 5 number of bits. 6 Rather, Applicant argues that Christopher's weighting circuits 504, 7 8 506, and 508 do not act on the input signal 500 but on signal 503 which has 9 already been shifted by barrel shifter 501 (FF 6). We understand Applicant 10 to argue that claim 5 requires that the plurality of parallel signal lines be 11 connected directly to the input signal without any intervening circuitry. We disagree that claim 5 is so limiting. 12 Claim 5 uses the construction "comprising" in the preamble. This 13 construction permits the inclusion of more than the recited elements. 14 Moreover, claim 5 does not require that the input signal be directly connected 15 to the plurality of parallel signal lines. Claim 5 recites "a plurality of signal 16 17 lines arranged parallel to each other for producing in advance signals that are 18 shifted a number of bits necessary for operating on said input signal." Claim 19 5 recites that the signal lines produce signals that are shifted a number of bits 20 necessary for operating on the input signal. The claim is broad enough to include an intervening circuit element between the input signal and the 21 parallel signal lines. Ultimately, both circuits could function to shift bits 22 necessary for operating on the input signal, as the Examiner found with 23

1	respect to Christopher (FFs 4, 5 and 8). For these reasons, Applicant's
2	argument is not persuasive.
3	Applicant next argues that Christopher's switch means does not
4	function to select outputs of the plurality of signal lines or an all "0" as
5	recited per claim 5. Specifically, Applicant argues:
6 7 8	In Christopher, control signals CO and C1 are input into gate 518. Gate 518 controls gating circuit 510, while control signal C1 controls gating circuits 512 and 514. These gating
9	circuits pass the signals from dividers 504, 506, and 508 to
10 11	summing circuit 516 depending on the control signals C0 and C1. However, there is no switch in Christopher with the
12 13	function recited in claim 5 for selecting outputs of said plurality of signal lines or all "0". (Br. 10).
14	of signal filles of all o. (Dr. 10).
15	Applicant's argument is conclusory and not meaningful. Merely
16	rehashing what the reference does show and concluding that it does not meet
17	a particular limitation is not helpful to the trier of fact and certainly does not
18	rise to the level of showing error in the Examiner's findings. Does the
19	Applicant contend that none of the gates function to select outputs from the
20	plurality of signal lines, and if so, why not? Or does Applicant contend that
21	Christopher does not function to select from an "all 0" and if so, why not? Or
22	does Applicant contend that Christopher does not function to do both - that
23	none of the gates function to select outputs from the plurality of signal lines
24	and select from an "all 0", and if so, why not?
25	The Examiner explicitly found that gating elements 510, 512, and 514
26	(FIG. 6) meet the switching means functional limitation since the outputs of

- the gating elements provide either signals in the parallel lines (when enabled)
- or otherwise zero (when disabled) to the adder 502 in response to control
- 3 signals CO and C1 (FFs 4 and 9). Applicant's argument is insufficient to
- 4 demonstrate error in the Examiner's specific findings.
- For all of these reasons, the Examiner's rejection of claim 5 is
- 6 sustained.
- 7 E. Decision
- 8 Upon consideration of the record, and for the reasons given, the
- 9 Examiner's rejection is affirmed.
- The Examiner's rejection of claims 5 under 35 U.S.C. § 102(b) as
- being anticipated by Christopher is affirmed.
- No time period for taking any subsequent action in connection with
- 13 this appeal may be extended under 37 C.F.R. § 1.136(a)(1)(iv) 2006.

14

#### **AFFIRMED**

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